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Docket No.: 2000P15141

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By: Loren D. Pearson Date: April 28, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/923,720 Confirmation No: 1125  
Applicant : Frank Pietzschmann  
Filed : August 6, 2001  
Art Unit : 2829  
Examiner : Emily Y. Chan  
Title : Test Apparatus for Semiconductor Circuit and Method of  
Testing Semiconductor Circuits  
Docket No. : 2000P15141  
Customer No. : 24131

LETTER TO THE OFFICIAL DRAFTSMAN

Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Attached please find seven (7) sheets of drawings in the above-identified application.

Please charge any fees due to Deposit Account No. 12-1099 of Lerner and Greenberg, P.A.

Respectfully submitted,

Loren Donald Pearson (Reg. No. 42,987)

Date: April 28, 2004

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